



SSC8L62GS3

N-Channel Enhancement Mode MOSFET

➤ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
60V	$\pm 20V$	9.2m Ω @10V	59A
		12.5m Ω @4V5	

➤ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

100% UIS + ΔV_{DS} + R_g Tested!

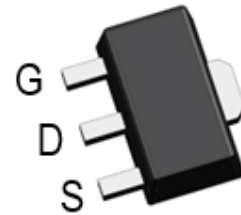
➤ Applications

- Motor Drive Control
- DCDC Conversion
- Power Supplies
- Synchronous Rectification

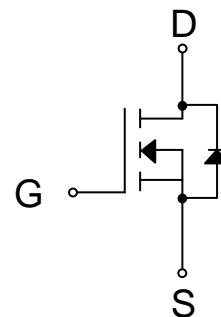
➤ Ordering Information

Device	Package	Shipping
SSC8L62GS3	SOT-89-3L	1000/Reel

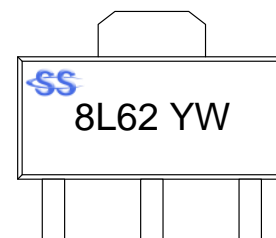
➤ Pin Configuration



SOT-89-3L (Top View)



Pin Configuration



Marking

(YW: Internal Traceability Code)

**➤ Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

Symbol	Parameter	Ratings	Unit
V_{DS}	Drain-to-Source Voltage	60	V
V_{GS}	Gate-to-Source Voltage	± 20	V
I_D	Continuous Drain Current ^d	$T_C=25^{\circ}\text{C}$	A
		$T_C=100^{\circ}\text{C}$	
I_{DSM}	Continuous Drain Current ^a	$T_A=25^{\circ}\text{C}$	A
		$T_A=70^{\circ}\text{C}$	
I_{DM}	Pulsed Drain Current ^b	236	A
P_D	Power Dissipation ^c	$T_C=25^{\circ}\text{C}$	W
		$T_C=100^{\circ}\text{C}$	
P_{DSM}	Power Dissipation ^a	$T_A=25^{\circ}\text{C}$	W
		$T_A=70^{\circ}\text{C}$	
I_{AS}	Avalanche Current ^b $L=0.5\text{mH}$ Single Pulse	18	A
E_{AS}	Avalanche Energy ^b $L=0.5\text{mH}$ Single Pulse	81	mJ
T_J	Operation junction temperature	$-55\sim 150$	$^{\circ}\text{C}$
T_{STG}	Storage temperature range	$-55\sim 150$	

➤ Thermal Resistance Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	27	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	2.4	

Note:

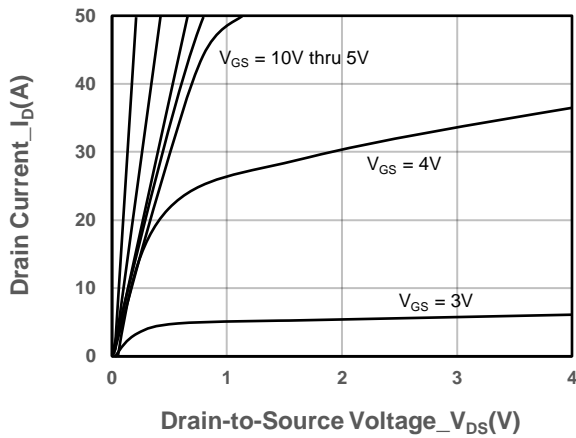
- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user is specific board design. The power dissipation is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

**➤ Electrical Characteristics (T_A=25°C unless otherwise noted)**

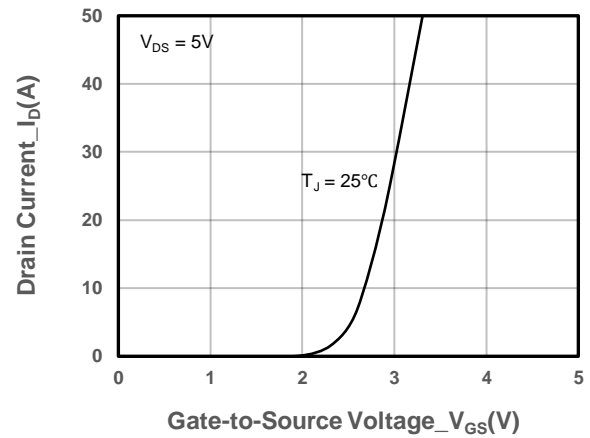
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	60			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	1	1.8	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 10A		9.2	12	mΩ
		V _{GS} = 4.5V, I _D = 5A		12.5	18	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Transconductance	G _{FS}	V _{DS} = 5V, I _D = 10A		30		S
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = 5A		0.8	1.3	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		1.4		Ω
Input Capacitance	C _{ISS}	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz		940		pF
Output Capacitance	C _{OSS}			350		
Reverse Transfer Capacitance	C _{RSS}			24		
Total Gate Charge	Q _G	V _{GS} = 10V, V _{DS} = 30V, I _D = 10A		16		nC
Gate to Source Charge	Q _{GS}			4.4		
Gate to Drain Charge	Q _{GD}			2.5		
Turn-on Delay Time	T _{D(ON)}	V _{GS} = 10V, V _{DS} = 30V, R _L = 1.5Ω, R _G = 3Ω		8		ns
Rise Time	T _r			4		
Turn-off Delay Time	T _{D(OFF)}			18		
Fall Time	T _f			4.1		
Diode Recovery Time	T _{rr}	I _F =20A, di/dt=100A/us		24		ns
Diode Recovery Charge	Q _{rr}	I _F =20A, di/dt=100A/us		54		nC



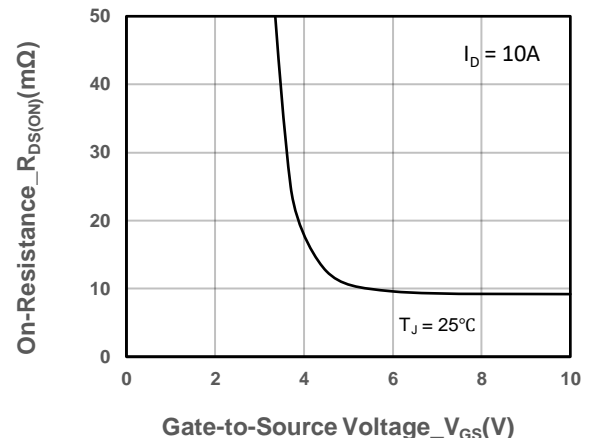
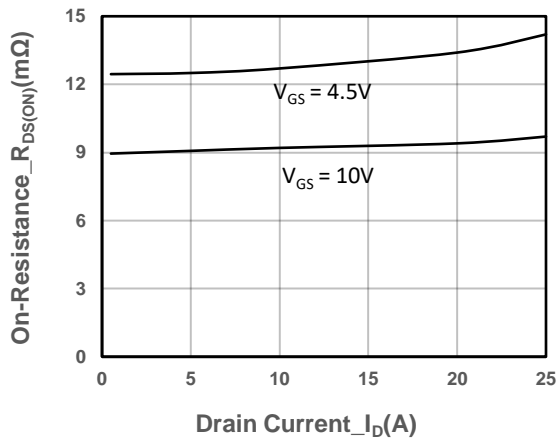
➤ **Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)**



Output Characteristics

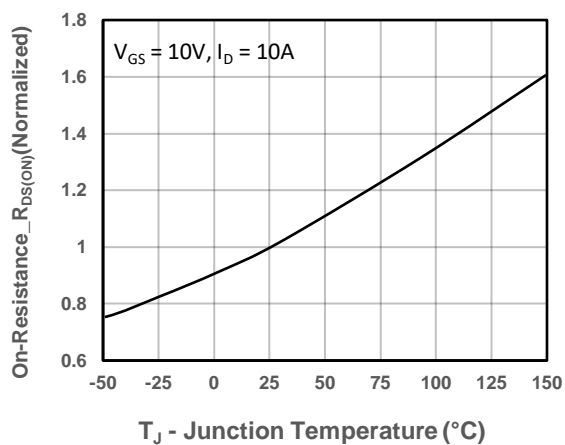


Transfer Characteristics

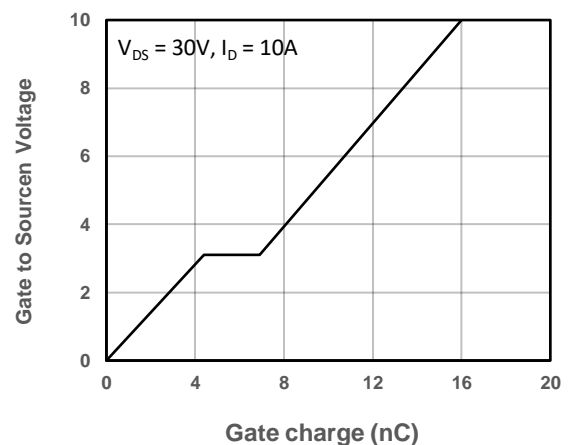


On-Resistance vs. Drain Current and Gate Voltage

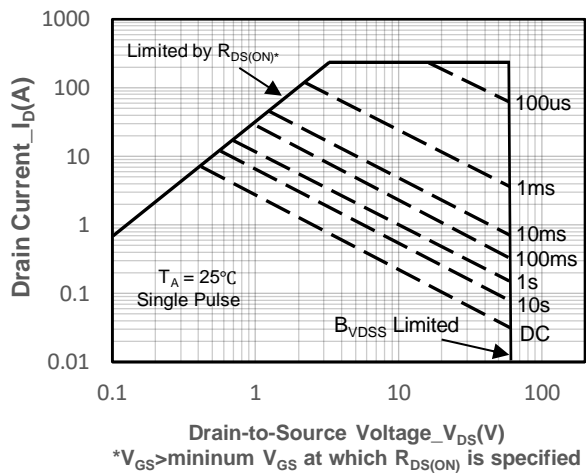
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature



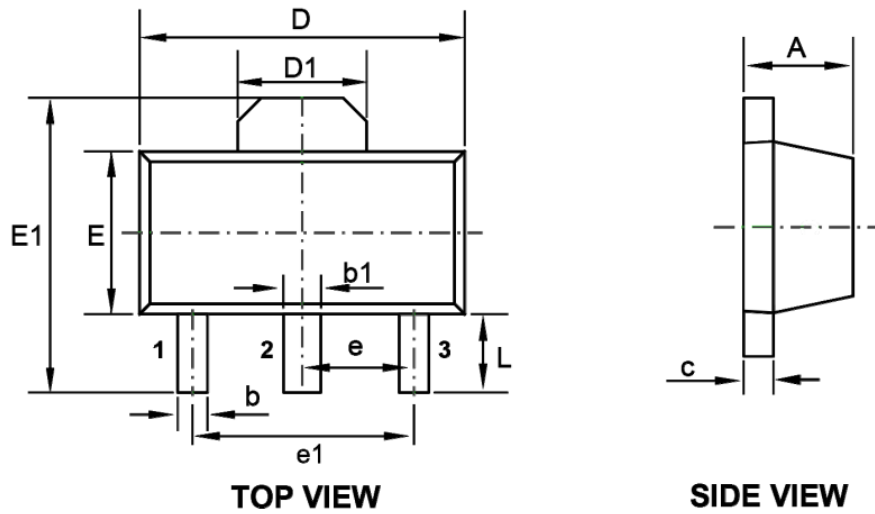
Gate-Source Voltage vs. Gate charge



Safe Operating Area vs. Junction-to-Ambient

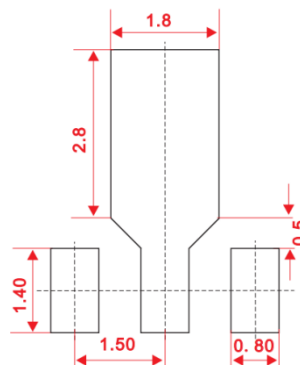
➤ Package Information

● Mechanical Data



DIM	Millimeters		
	Min.	Typ.	Max.
A	1.40	-	1.60
b	0.32	-	0.52
b1	0.40	-	0.58
c	0.35	-	0.44
D	4.40	-	4.60
D1	1.55 REF.		
E	2.30	-	2.60
E1	3.94	-	4.25
e		1.50	
e1		3.00	
L	0.90	-	1.20

● Recommended Pad outline (Unit: mm)





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